



In Association with



VLSI SYSTEMS Conclave

05.03.2024 & 06.03.2024



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05.03.2024 & 06.03.2024

Inaugural Function on 05.03.2024 @ 10.00 AM | Beta Hall



Mr. SAMUEL TENSINGH

Associate Lecturer - VLSI,
The University of Sydney, Australia

*has kindly consented to be the chief guest
and will deliver the Inaugural address*

Dr. R. Azhagumurugan
HOD/EEE,
IEEE PES TREASURER-MS

Dr. J. Raja
Dean (Academics)
& HOD/ECE

Dr. K. Porkumaran
Principal

Dr. Sai Prakash LeoMuthu
Chairman & CEO





VLSI SYSTEMS Conclave

05.03.2024 & 06.03.2024

05.03.2024 - HANDS ON SESSION ON ANALOG CIRCUIT LAYOUT



Mr. S. VINOTH KUMAR
Assistant Professor
Department of ECE
Sri Sairam Engineering College



Mr. N. BALAJI
Assistant Professor
Department of ICE
Sri Sairam Engineering College



Mrs. G. JAYANTHI
Associate Professor
Department of ICE
Sri Sairam Engineering College

06.03.2024 - SESSION ON PHYSICAL DESIGN AND A PANEL DISCUSSION



Mr. S. HARIHARAN
Freelancer & Adjunct Faculty
IITDM, Kancheepuram



Mr. T. A. BHARATHWAJ
Principal Design Engineer
Microchip Technology Inc.
Chennai

Dr. R. Azhagumurugan
HOD/EEE,
IEEE PES TREASURER-MS

Dr. J. Raja
Dean (Academics)

Dr. K. Porkumaran
Principal

Dr. Sai Prakash LeoMuthu
Chairman & CEO



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